Case Docket No. IMEQ 8.001AUS Date: September 26, 2003

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In re application of:

Vanhaelemeersch, et al.

App. No.

10/078,284

Filed

February 15, 2002

For

METHOD FOR FABRICATING A

SEMICONDUCTOR DEVICE

Examiner

Estrada, M.

Art Unit

2823

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box

1450, Alexandria, VA 22313-1450, on

CERTIFICATE OF MAILING

September 26, 2003

(Date)

Rose M. Thiessen, Reg. No. 40,202

TED STATES PATENT AND TRADEMARK OFFICE

Box 2327

ngton, VA 22202

Transmitted herewith is a Response to Restriction Requirement in the above-identified application Requirement in the above-identified Requirement in the above-identified Requirement in the above-identified Requirement in the above-identified Requireme UNITED STATES PATENT AND TRADEMARK OFFICE P.O. Box 2327

Arlington, VA 22202

Sir:

The fee has been calculated as shown below:

FEE CALCULATION												280	
FEE TYPE					FEE CODE		CALCULATION					TOTAL	
Total Claims > 20 or Previously Paid	36	-	36	=	0	1202	(\$18)	0	х	18	=	0	\$0
Independent > 3 or Previously Paid	3	-	3 ·	=	0	1201	(\$84)	0	х	84	=	0	\$0
								TOTAL FEE DUE				\$0	

- (X) Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.
- (X) Please use Customer No. 20,995 for the correspondence address.
- (X) A return prepaid postcard.

Rose M. Thiessen

Registration No. 40,202

Attorney of Record

Customer No. 20,995

(619) 235-8550

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

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September 26, 2003

Rose M. Thiessen, Reg. No. 40,202

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In an Office Action mailed August 26, 2003, the Examiner imposed a Restriction Requirement. In connection therewith, the Examiner asserted that the application claimed the following inventions: Claims 1-30, drawn to a method of making a semiconductor device and a semiconductor device (Group I); and Claims 31-36, drawn to gaseous mixture (Group II).

Applicants hereby elect the invention of Group I, including Claims 1-30, drawn to a method of making a semiconductor device and a semiconductor device, without traverse and without prejudice. The Examiner is invited to call the undersigned attorney if it is desirable to discuss any issues raised in this Response.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

By:

Rose M. Thiessen Registration No. 40,202

Attorney of Record Customer No. 20,995

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